Attorney's Docket No.: 10559-602001 / P12886

Intel Corporation



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Wheeler, et al.

Art Unit: 2123

Serial No.:

09/994,574

Examiner: Russell L. Guill

Filed:

November 26, 2001

Title:

UNIFIED DESIGN PARAMETER DEPENDENCY MANAGEMENT

METHOD AND APPARATUS

MAIL STOP AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Applicants request consideration of the reference listed on the attached PTO-1449 form. A copy of the reference is attached for your reference.

This statement is being filed after a final Office action or a Notice of Allowance, but before payment of the issue fee. A check for \$180 in payment of the late submission fee of §1.17(p) is enclosed. Please apply any other charges or credits to Deposit Account No. 06-1050.

Date: July 13, 2006

Respectfully submitted,

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I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Sheet	1	of	1
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Substitute Form PTO-1449 (Modified) U.S. Department of Comme Patent and Trademark Off		Application No. 09/994,574	
Information Disclosure Statement By Applicant	Applicant William R. Wheeler, et al.		
JUL 1 7 2006 (Use several sheets if necessary) (37 CFR §1.98(b))	Filing Date November 26, 2001	Group Art Unit 2123	

TRADEN			U.S. Patent	Documents			
Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
	AA						
	AB						
	AC						

	Foreign Patent Documents or Published Foreign Patent Applications							
Examiner	Desig.	Document	Publication	Country or			Trans	lation
Initial	ID	Number	Date	Patent Office	Class	Subclass	Yes	No
	AD							
	AE	*						
	AF							
	AG			•				
!	АН							

	Other Documents (include Author, Title, Date, and Place of Publication)				
Examiner	Desig.				
Initial	ID	Document			
	AI	Aihara, Masami, et al., Á Mixed Level Simulator mega-FAL with Novel Data Structure Oriented to HDL Statements" Custom Integrated Circuits Conference, IEEE 1990, pgs 10.3.1 – 10.3.4			
	AJ				
	AK				
	AL				

Examiner Signature	Date Considered
EXAMINER: Initials citation considered. Draw line through citation if no	ot in conformance and not considered. Include copy of this form with

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